

REMARKS

The Examiner rejected claims 1-3, 9, 12, 19, 20, 23, 30, 35, and 40-43 under 35 U.S.C. 102(b) as being anticipated by “U.S. Patent No. 6,065,080 to Barefield et al.” It appears that the Examiner misidentified the U.S. Patent Number of Barefield *et al.* Applicants assume that the Examiner rejected the above claims as being anticipated by U.S. Patent No. 5,739,791 to Barefield *et al.* If Applicants’ assumption is incorrect, then please provide the correct U.S. Patent Number of Barefield *et al.*

Applicants have amended independent claims 1, 2, 19, 30, and 40. In addition, Applicants have canceled claim 35.

Independent claims 1, 2, 19, 30 and 40 now require that a memory module include electrical terminals for interfacing with a second circuit board, which includes a radio receiver. Claims 1 and 2 also require that the memory module include a radio transmitter that can transmit information related to the number of rows on the memory module and the number of columns on the memory module to the radio receiver mounted on the second circuit board thereby enabling a device on the second circuit board to utilize such information to write data to a volatile memory device on the memory module. It is Applicants’ understanding that none of the cited prior art taken in any combination teaches the above features. In particular, the cited prior art does not teach utilizing a radio transmitter to communicate between two circuit boards that are connected via electrical terminals. In addition, the cited prior art does not teach utilizing a radio transmitter to communicate information related to the number of rows and columns in a memory module so that that information can be utilized to write data to a volatile memory device on the memory module. Thus, Applicants believe that claims 1-3, 9, 12, 19, 20, 23, 30, and 40-41 are allowable over the art of record.

The Examiner rejected claims 4 – 7 under 35 U.S.C. 103(a) as being unpatentable over Barefield *et al.* in view of U.S. Publication 2002/0118175 to Liebennow *et al.*

Applicants have amended independent claim 2. As discussed above, Applicants believe that neither Barefield *et al.* nor Liebennow *et al.* disclose the features of amended claim 2, from which claims 4 – 7 depend. Thus, Applicants believe that claims 4 – 7 are allowable over the art of record.

The Examiner rejected claim 8 under 35 U.S.C. 103(a) as being unpatentable over Barefield *et al.* in view of U.S. Publication 2002/0101907 to Dent *et al.*

Applicants have amended independent claim 2, from which claim 8 depends. As discussed, above, Applicants believe that neither Barefield *et al.* nor Dent *et al.* disclose the features of amended claim 2 from which claim 8 depends. Thus, Applicants believe that claim 8 is allowable over the art of record.

The Examiner rejected claims 10, 11, 13 – 18, 21, 22, 24 – 29, 31 – 34, and 36 - 39 under 35 U.S.C. 103(a) as being unpatentable over Barefield *et al.* in view of Applicants' admitted Prior Art. Applicants believe that neither Barefield *et al.* nor the admitted prior art disclose the features of the amended claim 2 from which claims 10, 11, and 13 – 18, depend. Thus, Applicants believe that claims 10, 11, and 13 – 18 are allowable over the art of record.

Similarly, independent claim 19, from which claims 21, 22, and 24 - 29 depend, now requires that a memory module include electrical terminals for interfacing with a second circuit board, which includes a radio receiver. In addition, claim 19 now requires that the memory module include a radio transmitter that can transmit information related to the number of rows on the memory module and the number of columns on the memory module to the radio receiver mounted on the second circuit board thereby enabling a device on the second circuit board to

utilize said information to write data to a volatile memory device on the memory module.

Neither the cited prior art nor the admitted prior art disclose such features. Thus, Applicants believe that claims 21, 22, and 24 – 29 are allowable.

Similarly, independent claim 30, from which claims 31 - 34 depend, now requires that a memory module include electrical terminals for interfacing with a second circuit board, which includes a radio receiver. In addition, claim 30 now requires that the memory module include a radio transmitter that can transmit information related to the number of rows on the memory module and the number of columns on the memory module to the radio receiver physically coupled to the second circuit board thereby enabling a device on the second circuit board to utilize said information to write data to a volatile memory device on the memory module. Neither the cited prior art nor the admitted prior art disclose such features. Thus, Applicants believe that claims 31 – 34 are allowable.

Applicants have canceled claims 35 – 39. Thus, the Examiner's rejections of those claims are moot.

The Examiner rejected claims 48 - 51 under 35 U.S.C. 103(a) as being unpatentable over Barefield *et al.* in view of U.S. Patent No. 4,870,704 to Matelan *et al.* As discussed above, Applicants believe that neither Barefield *et al.* nor Matelan *et al.* disclose the features of amended claim 1 from which claims 48 - 51 depend. Thus, Applicants believe that claims 48 - 51 are allowable over the art of record.

CONCLUSION

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

By Hoyt A. Fleming III
Hoyt A. Fleming III
Registration No. 41752

Date: August 22, 2003

Address correspondence to: <input checked="checked" type="checkbox"/> <i>Customer Number or Bar Code Label</i> 28422	or <input type="checkbox"/> <i>Correspondence Address Below</i> Park, Vaughan & Fleming LLP P.O. Box 140678 Boise, ID 83714	Direct telephone calls to: Hoyt A. Fleming III (208) 336-5237
--	--	--